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S O L U T I O N S

Processor Performance, Packaging and Reliability Utilizing a Phase Change Metallic Alloy Thermal Interface System

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Outline

- Testing
- Performance Data
- Packaging & Reliability
- Next Steps
- Summary



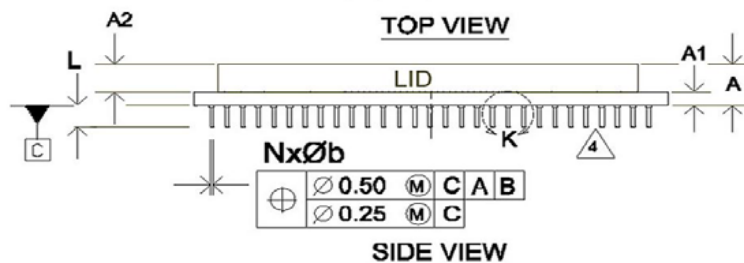
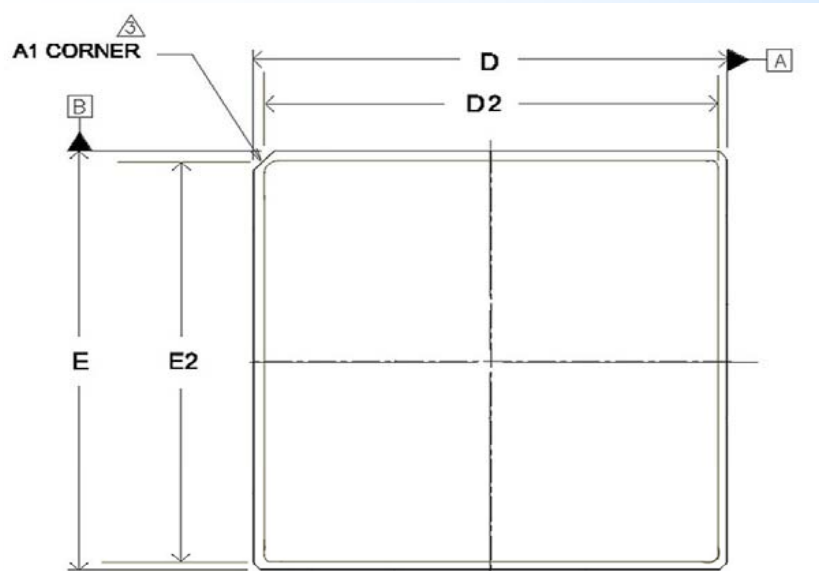
Testing Overview

- TIM1 interface tested
- Thermal Test Vehicles (TTVs)
- Θ_{jc} calculated
- Comparative data



Testing

Thermal Test Vehicles (TTVs)



- 1.4 cm² die area
- 80 Watts
- Uniform heat flux (57 W/cm²)
- FCPGA package
- Ni-plated Cu lid



Testing

Value of TTV Testing

- Actual chip and lid surface finish, flatness and composition
- Simulate processor hot spots
- Test lid attach process qualities
- Assemblies can be environmentally stressed without disturbing interface



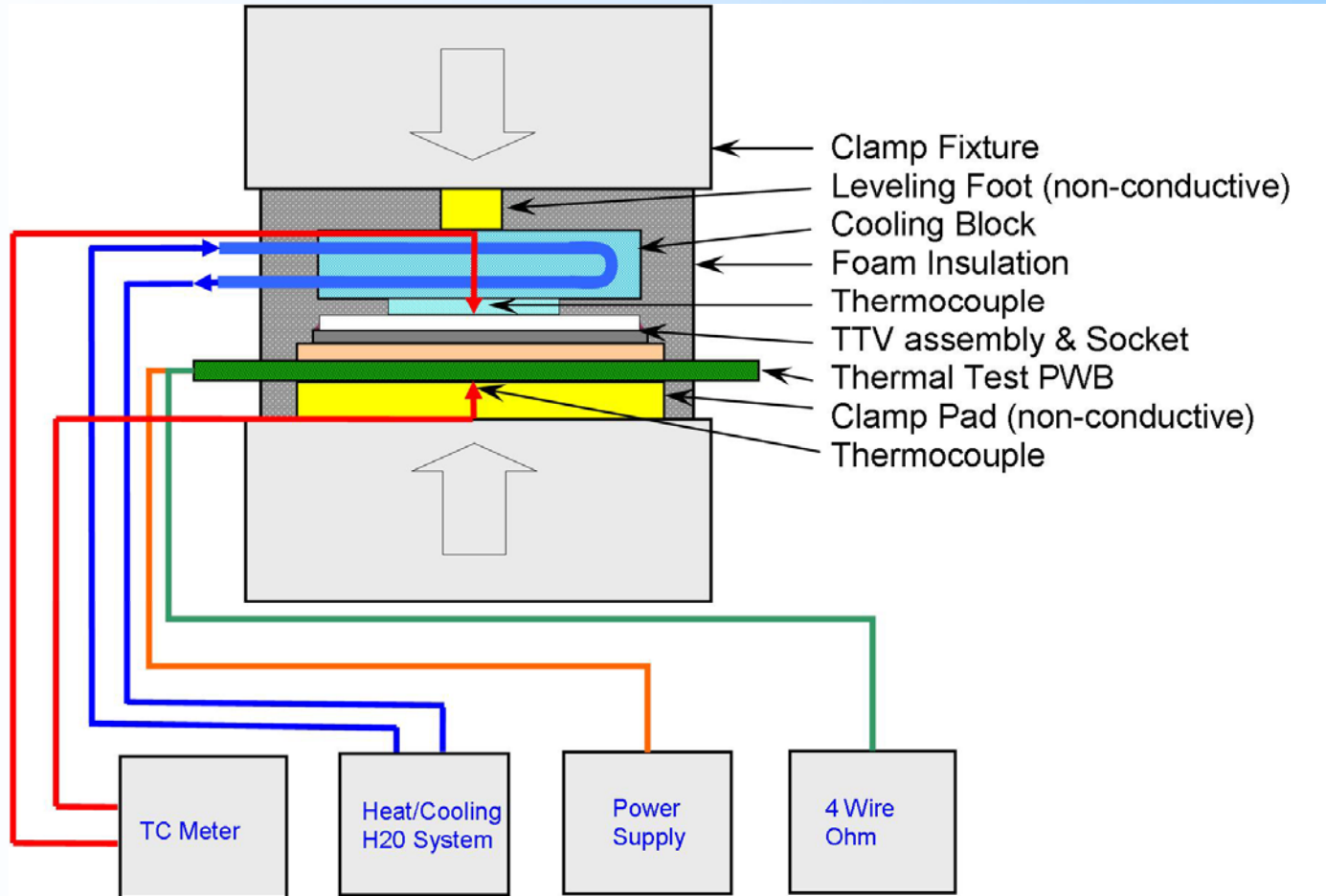
Testing TIMs

	<i>Shin-Etsu X23-7783D</i>	<i>Chomerics T557</i>	<i>Enerdyne Indigo1</i>	<i>Indium Solder</i>
TIM Type	Grease	PSH	PCMA	Solder
Composition	Al-filled polymer	PCMA within polymer	Indium- based	Indium
Phase Change Temperature (°C)	N/A	43 / 65	~65	156
Impedance (°C-cm ² /W)	~ 0.07 (1 mil BLT*)	0.0625 (70°C, 50 psi)	< 0.04 (80°C, 40 psi)	0.07-0.08 (8-9 mil BLT)
Conductivity (W/mK)	6.0	3.0	> 20	80

* Extrapolated from BLT vs. impedance graph



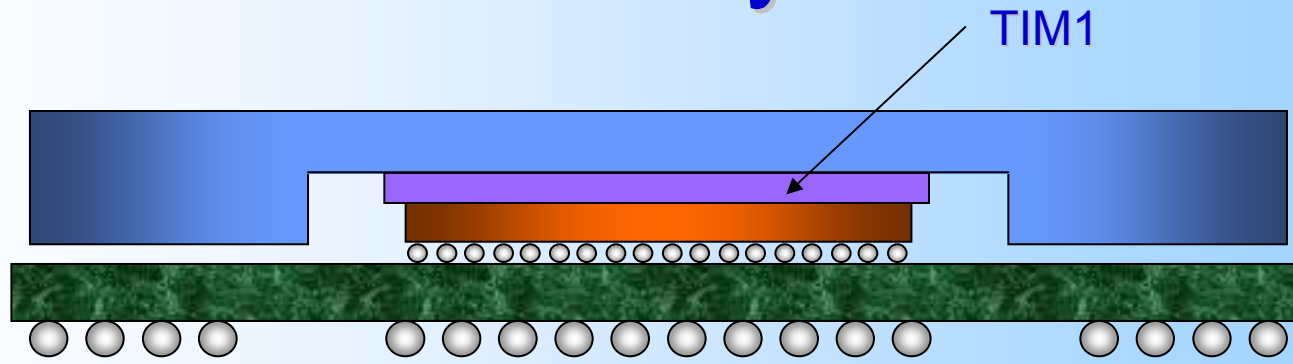
Testing Platform





Testing

TTV Assembly



- 10 lbs force
- No lid seal
- Minimum Bondline Thickness (BLT)
- Gold metallization on Indium TTV

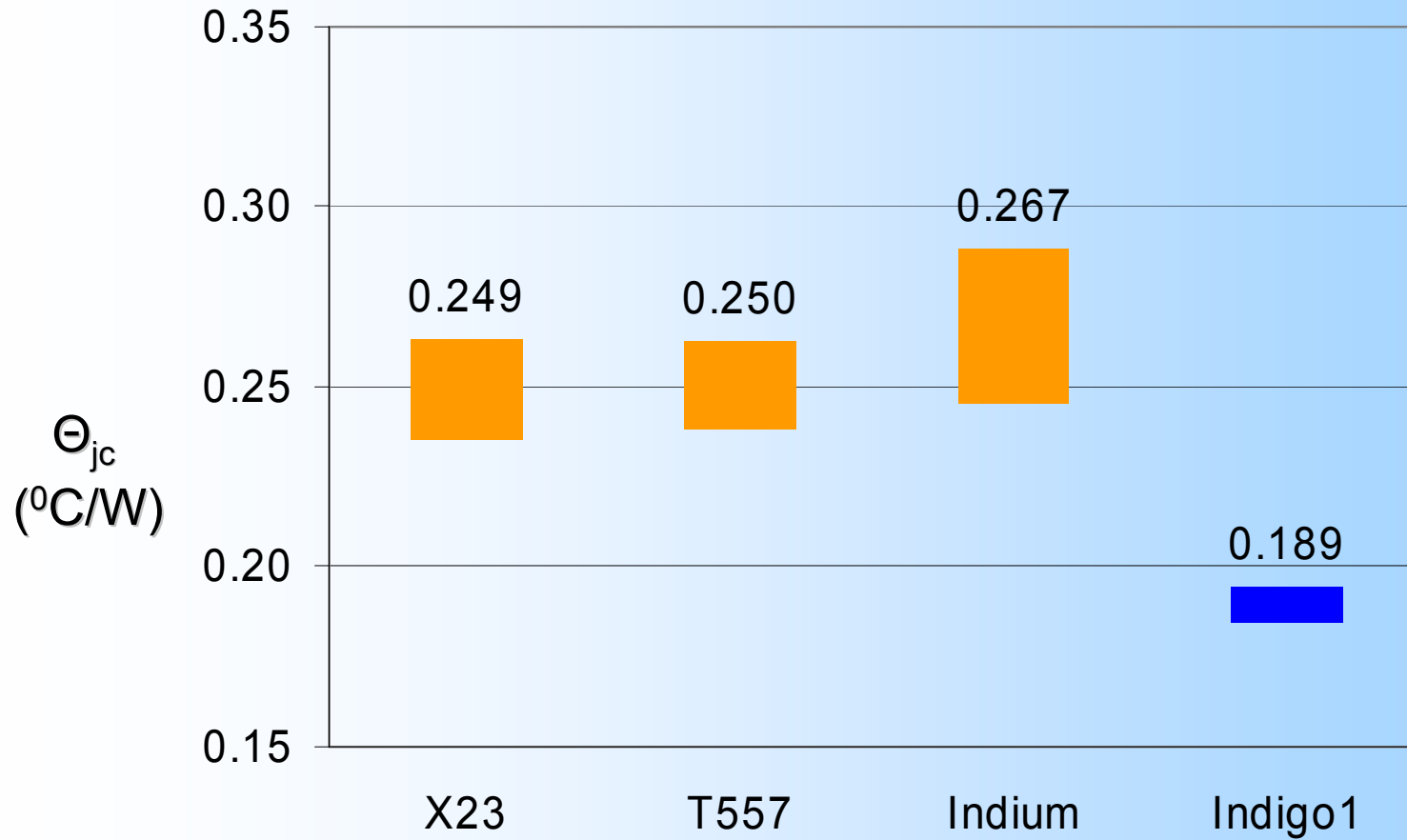


Testing Methodology

- Calibration
- Bias of die
- Power, diode resistance & case temperature measured
- 3 measurements / sample
- Θ_{jc} calculated



Performance Data





Packaging & Reliability

Historic TIM Qualities

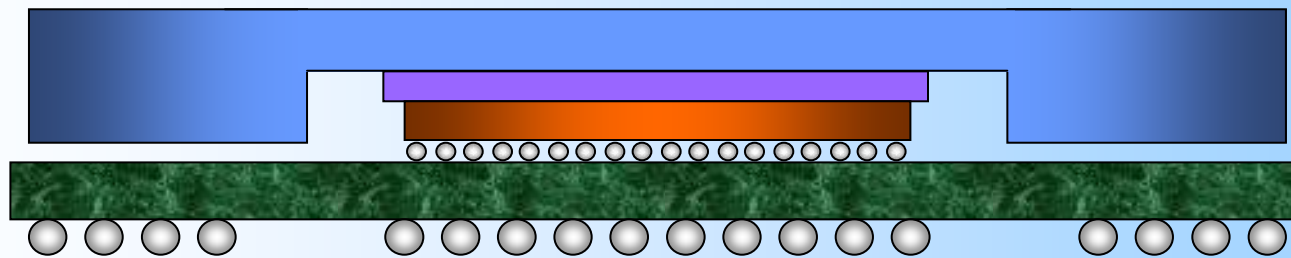
<i>Material</i>	<i>Advantages</i>	<i>Disadvantages</i>
<i>Thermal Grease</i>	<ul style="list-style-type: none"> ■ High bulk conductivity ■ Conforms to surface irregularities ■ No cure ■ Reworkable 	<ul style="list-style-type: none"> ■ Pump-out ■ Phase separation ■ Migration
<i>Polymer-solder Hybrid (PSH)</i>	<ul style="list-style-type: none"> ■ Good bulk conductivity ■ Conforms to surface irregularities 	<ul style="list-style-type: none"> ■ Cure needed ■ Reflow needed ■ Delamination ■ Non-reworkable
<i>Phase-Change Metal Alloy (PCMA)</i>	<ul style="list-style-type: none"> ■ High (metal) bulk conductivity ■ Easy handling ■ Reworkable 	<ul style="list-style-type: none"> ■ Reflow needed ■ Pump-out ■ Migration ■ Voiding ■ Oxidation
<i>Indium solder</i>	<ul style="list-style-type: none"> ■ High (metal) bulk conductivity ■ Easy handling ■ No pump-out ■ No migration 	<ul style="list-style-type: none"> ■ Needs Au-plate for wettability ■ Reflow needed ■ Stress cracking, delamination ■ Voiding

Source: A. Dani, J. Matayabas, P. Koning, "Thermal interface material technology advancements and challenges—an overview", ASME InterPACK 2005, San Francisco, CA., July 17-22, 2005



Packaging & Reliability

Overview

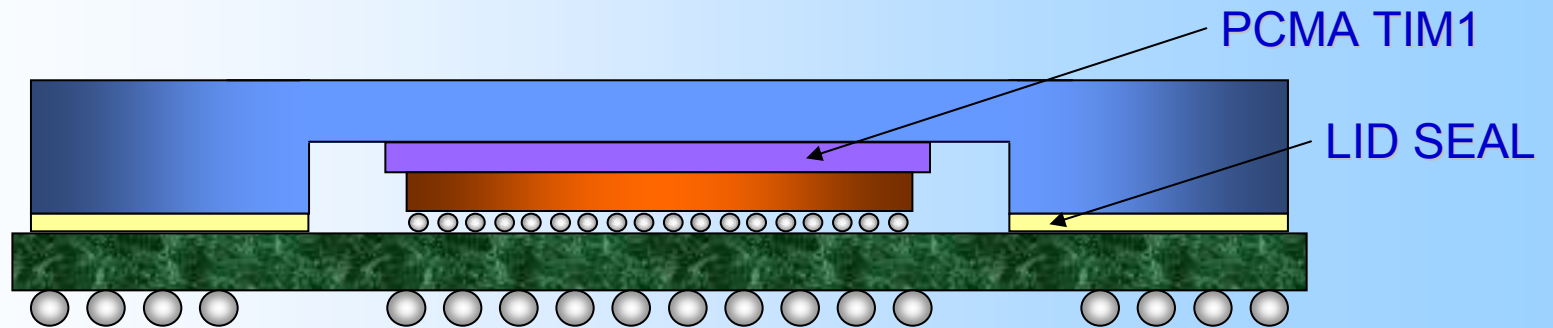


- Corrosion
- Migration
- Diffusion



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Corrosion Mitigation



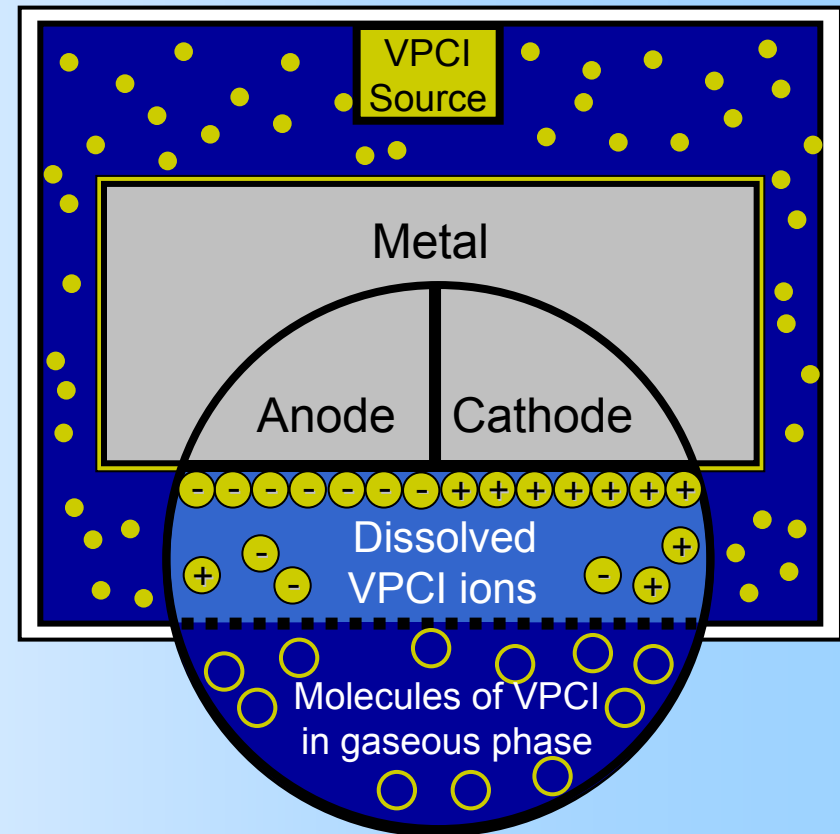
- Vent-free continuous lid seal
- Sealant with low vapor transmission rate
- Compatible with PCMA burn-in during lid attach



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Corrosion Mitigation

- Lid seal facilitates controlled environ. within lid cavity
- Vapor Phase Corrosion Inhibitor (VPCI)
- VPCI ions on PCMA surface interrupt corrosion cell





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Corrosion Mitigation

Indigo



1800 temp cycles

PCMA without mitigants



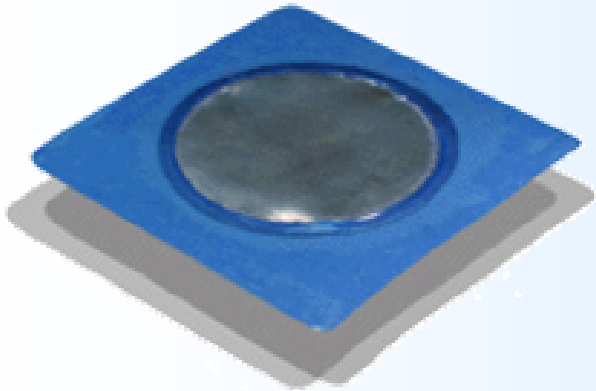
50 temp cycles

Temperature cycling—Service Condition “B”
-55°C to +125°C



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Migration Control



- Deployment of perimeter barrier
- Secondary containment by lid seal
- Passed shock test (Service Condition “E”)
- Passed vibration test (Service Condition “4”)



Packaging & Reliability

Diffusion

Material	$D_{100^{\circ}\text{C}}$ (cm ² /sec)	Penetration Depth after 10 Years
Aluminum	4.06×10^{-47}	<1 nm
Copper	1.22×10^{-15}	~ 10 μm
Gold	1.50×10^{-18}	< 1 μm
Indium	3.30×10^{-52}	<1 nm
Bismuth	7.23×10^{-60}	<1 nm

- Insufficient energy at typical operating temperatures for measurable diffusion
- Significantly more diffusion of AuSn from eutectic die bonding



Next Steps

- Complete TTV environ. tests
- Reduce voids to <2%
- Further customer qualification
- TIM2 development



PCMA Thermal Interface Summary

- 25-30% reduction of Θ_{jc}
- No chip metallization required
- Corrosion mitigation demonstrated
- Migration controlled
- Negligible diffusion in Silicon



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Thank you.

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